

**NEW UTILITY PATENT APPLICATION
TRANSMITTAL***(Only for new nonprovisional applications under 37 C.F.R. 1.53(b))*Docket No.
R2184.0078/P078Total pages in this
submission**TO THE ASSISTANT COMMISSIONER FOR PATENTS****Box Patent Application
Washington, D.C. 20231**

Transmitted herewith for filing under 35 U.S.C. 111(a) and 37 C.F.R. 1.53(b) is a new utility patent application for an invention entitled:

ARBITRATION METHOD OF A BUS BRIDGE

and invented by:

Kohji Kameda

IF A CONTINUATION APPLICATION, check appropriate box and supply requisite information:☐ Continuation ☐ Divisional☐ Continuation-in-part (CIP) of prior application No.: _____

Enclosed are:

Application Elements

1. ☒ Filing fee as calculated and transmitted as described below
2. ☒ Specification having 23 page(s) and including the following:
 - a. ☒ Descriptive title of the invention
 - b. ☐ Cross references to related applications (*if applicable*)
 - c. ☐ Statement regarding Federally-sponsored research/development (*if applicable*)
 - d. ☐ Reference to microfiche appendix (*if applicable*)
 - e. ☒ Background of the invention
 - f. ☒ Brief summary of the invention
 - g. ☒ Brief description of the drawings (*if drawings filed*)
 - h. ☒ Detailed description
 - i. ☒ Claims as classified below
 - j. ☒ Abstract of the disclosure

Application Elements (continued)

3. ☒ Drawing(s) (when necessary as prescribed by 35 U.S.C. 113)
☒ Formal ☐ Informal Number of sheets: 2
4. ☒ Oath or Declaration
 a. ☒ Newly executed (original or copy) ☐ Unexecuted
 b. ☐ Copy from a prior application (37 C.F.R. 1.63(d) (for continuation/divisional applications only)
 c. ☒ With Power of Attorney ☐ Without Power of Attorney
5. ☐ Incorporation by reference (usable if Box 4b is checked)
 The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Box 4b, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.
6. ☐ Computer program in microfiche
7. ☐ Genetic sequence submission (if applicable, all must be included)
 a. ☐ Paper copy
 b. ☐ Computer readable copy
 c. ☐ Statement verifying identical paper and computer readable copies

Accompanying Application

8. ☒ Assignment papers (cover sheet & document(s))
9. ☐ 37 C.F.R. 3.73(b) statement (when there is an assignee)
10. ☐ English translation document (if applicable)
11. ☐ Information Disclosure Statement/PTO-1449 ☐ Copies of IDS citations
12. ☐ Preliminary Amendment
13. ☒ Acknowledgment postcard
14. ☒ Certified copy of priority document(s) (if foreign priority is claimed)
15. ☐ Certificate of Mailing
☐ First Class ☐ Express Mail (Label No.: _____)
16. ☐ Small Entity statement(s) -- # submitted _____ (if Small Entity status claimed)

Accompanying Application (continued)

- 17.
- ☐
- Additional enclosures (please identify below):

Fee Calculation and Transmittal

The filing fee for this utility patent application is calculated and transmitted as follows:

☒ Large Entity☐ Small Entity

CLAIMS AS FILED					
For	# Filed	# Allowed	# Extra	Rate	Fee
Total Claims	7	- 20 =		x \$18.00	
Independent Claims	2	- 3 =		x \$78.00	
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
Other Fees (specify purpose): Recordation Form Cover Sheet					\$40.00
BASIC FEE					\$690.00
TOTAL FILING FEE					\$730.00

☒ A check in the amount of \$730.00 to cover the total filing fee is enclosed.☒ The Commissioner is hereby authorized to charge and Deposit Account No. 4 - 1073 as described below. A duplicate copy of this sheet is enclosed.☐ Charge the amount of _____ as filing fee.☒ Credit any overpayment.☒ Charge any additional filing fees required under 37 C.F.R. 1.16 and 1.17.☐ Charge the issue fee set in 37 C.F.R. 1.18 at the mailing of the Notice of Allowance, pursuant to 37 C.F.R. 1.31(b).

Jon Glassman Reg. No. 32,699 for

Dated: July 3, 2000

Mark J. Thronson
 Attorney Reg. No.: 33,082
 Dickstein Shapiro Morin & Oshinsky LLP
 2101 L Street NW
 Washington, DC 20037-1526
 (202) 785-9700

SPECIFICATION

TO ALL WHOM IT MAY CONCERN:

BE IT KNOWN THAT I, KOHJI KAMEDA, a citizen
of Japan residing at Hyogo, Japan have invented
certain new and useful improvements in

ARBITRATION METHOD OF A BUS BRIDGE

of which the following is a specification:-

BACKGROUND OF THE INVENTION

1. Field of the Invention

The present invention generally relates to an arbitration method of a bus bridge and, more particularly, to an arbitration method performed by a bus bridge such as a PCI-PCI bridge of personal computers, a PCI-card bus bridge, a PCI-IEEE1394 (OHCI-Link) bridge, etc.

2. Description of the Related Art

The performance of computers has been accelerated every year, and peripheral devices such as memory devices are sped up and upgraded. The interface used for the computers and the peripheral devices includes the IEEE1394 interface. The IEEE1394 interface is standardized as a high-speed serial bus for the next generation's multimedia by IEEE, which can be extended over the consumer and the computer.

In the IEEE1394 standard, a data transfer rate from 100 Mbps to 400 Mbps is used. The IEEE1394 allows use of an isochronous transfer so as to guarantee a real-time operation using the isochronous transfer. That is, because the priority right is assigned each 125 μ s in the isochronous transfer of IEEE1394, the real time operation for the data transfer can be guaranteed.

The above-mentioned IEEE-1394 standard is not

for I/O devices but for buses. When the IEEE-1394
standard is used for a system, a bus bridge is provided
so as to interface a primary side bus with a secondary
side bus, the primary side bus being a local bus in the
5 system.

There might be a case in which only a limited
number of bus bridges can be provided in a system, and
the number of buses to be connected in the system is
greater than the limited number of bus bridges. In such
10 a case, two or more buses may be connected to a single
bus bridge by adding a unique ID to each of the buses.

There is a bridge device that interfaces a
primary side bus, which is a local bus in the system,
with a secondary side bus. Such a bridge device may
15 supports different operations with respect to the bus
control. For instance, one of the operations relates to
the serial bus according to the IEEE1394 standard and
the other operation relates to the bus according to a
standard other than the IEEE1394 standard.

When the bus in accordance with the IEEE1394
standard begins accessing the primary side bus, it takes
a relatively long time to transfer data. Therefore, the
IEEE1394 bus occupies the access right. Moreover, when
arbitration is performed, the access right might not be
25 handed over to other secondary side buses when priority

is given to the IEEE1394 side.

SUMMARY OF THE INVENTION

It is a general object of the present
5 invention to provide an arbitration method of a bus
bridge in which the above-mentioned problems are
eliminated.

A more specific object of the present
invention is to provide an arbitration method of a bus
10 bridge which equally gives an access right to secondary
side buses when the secondary side buses include the
IEEE1394 bus so that the secondary side buses other than
the IEEE1394 bus is prevented from being not given the
access right.

15 In order to achieve the above-mentioned
objects, there is provided according to one aspect of
the present invention an arbitration method of a bus
bridge which interfaces a primary side bus with a
plurality of secondary side buses, the primary side bus
20 being a local bus in a system and the secondary side
buses being external buses connected to the system, the
bus bridge supporting a plurality of kinds of operations
one of which is an operation related to a serial bus in
accordance with IEEE1394, the arbitration method
25 comprising the step of giving an access right

sequentially to each of the secondary side buses, when access demands to the primary side bus are lodged from more than two of the secondary side buses at the same time, by not giving a priority to any one of the
5 secondary side buses.

According to the present invention, since the access right is given sequentially to each of the secondary side buses, the access right can be given equally to each of the secondary side buses.

10 In one embodiment, one of the secondary side buses may be the serial bus in accordance with IEEE1394, and the rest of the secondary side buses may be card buses.

The arbitration method according to the
15 present invention may further comprise the step of: giving a priority right to the serial bus in accordance with IEEE1394; and maintaining the access right given to the serial bus in accordance with IEEE1394 when an access demand is lodged from the secondary side buses
20 other than the serial bus in accordance with IEEE1394.

Accordingly, when the serial bus in accordance with IEEE1394 is given an access right, an operation with respect to the serial bus in accordance with IEEE1394 can be completed without interruption even if
25 an access demand is lodged from other secondary buses

since the access right is not transferred from the serial bus in accordance with IEEE1394 to other secondary buses. Thus, the operation of the serial bus in accordance with IEEE1394 is prevented from being
5 failed.

In the arbitration method according to the present invention, the secondary side buses may include a plurality of card buses, and the arbitration method may comprise the steps of: performing an arbitration
10 between the serial bus in accordance with IEEE1394 and the card buses when access demands are lodged from the serial bus in accordance with IEEE1394 and the card buses; and performing an arbitration between the card
15 card busses. Additionally, the arbitration method may further comprise the step of changing an order of giving the access right.

When the secondary side buses include the serial bus in accordance with IEEE1394 and more than two
20 card buses, an arbitration circuit becomes complex. However, according to the present invention, the arbitration circuit can be simplified by dividing the operation of arbitration into two steps.

In the above-mentioned structure, there may be
25 a case in which the access right cannot be given equally

to the serial bus in accordance with IEEE1394 and the card buses. However, the priority can be given equally to each of the secondary side buses by flexibly changing the priority level in the first step. Thus, an
5 efficient arbitration can be achieved in response to a condition of the system.

The arbitration method according to the present invention may further comprise the step of giving a highest priority to the primary side bus when
10 the primary side bus lodges an access demand to the secondary side buses irrespective of a condition of arbitration between the secondary side buses.

According to the above-mentioned invention, the operation of arbitration is prevented from being
15 failed due to a collision of an access from the secondary side buses with an access from the primary side bus. That is, when the arbitration is performed with respect to the secondary side buses, the primary side bus is in an idle state. Accordingly, it is
20 possible that an access from the secondary side buses and a access from the primary side bus are made simultaneously, which may result in an operation error. However, such a problem can be eliminated by giving a highest priority to the primary side bus when the
25 primary side bus lodges an access demand to the

secondary side buses.

Additionally, there is provided according to another aspect of the present invention an arbitration method of a bus bridge which interfaces a primary side
5 bus with a plurality of secondary side buses, the primary side bus being a local bus in a system and the secondary side buses being external buses connected to the system, the bus bridge supporting a plurality of kinds of operations one of which is an operation related
10 to a serial bus in accordance with IEEE1394, the arbitration method comprising the step of giving a highest priority to the primary side bus when the primary side bus lodges an access demand to the secondary side buses irrespective of a condition of an
15 arbitration between the secondary side buses.

According to the above-mentioned invention, the operation of arbitration is prevented from being failed due to a collision of an access from the secondary side buses with an access from the primary
20 side bus. That is, when the arbitration is performed with respect to the secondary side buses, the primary side bus is in an idle state. Accordingly, it is possible that an access from the secondary side buses and a access from the primary side bus are made
25 simultaneously, which may result in an operation error.

However, such a problem can be eliminated by giving a highest priority to the primary side bus when the primary side bus lodges an access demand to the secondary side buses.

5 Other objects, features and advantages of the present invention will become more apparent from the following detailed description when read in conjunction with the accompanying drawings.

10 BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a block diagram of a bus bridge according to a first embodiment of the present invention;

15 FIG. 2 is a timing chart of signals in the first embodiment of the present invention; and

FIG. 3 is a block diagram of an arbiter according to the first embodiment of the present invention.

20 DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

A description will now be given, with reference to FIG. 1, of a first embodiment of the present invention.

25 FIG. 1 is a block diagram of a bus bridge according to the first embodiment of the present

invention. In FIG. 1, the bus bridge 3 corresponding to a bus master is connected to a primary side bus 21 which is a peripheral component interconnect (PCI) bus. Other bus masters 1a, 2b and 10 are also connected to the
5 primary side bus 21. An IEEE1394 bus 22 is connected to an IEEE1394 block 35 provided in the bus bridge 3. The bus bridge 3 is also provided with two card-bus blocks 33 and 34 which have card slots A and B, respectively so that a card bus 23A is inserted into the card slot A and
10 a card bus 23B is inserted into the card slot B. The IEEE1394 bus and the card buses 23A and 23b correspond to the secondary side buses.

Each of the card-bus blocks 33 and 34 serves as an interface in accordance with a card bus protocol.
15 The IEEE1394 block 35 performs a bus controlling operation in accordance with the IEEE 1394 protocol, and also serves as an interface of the IEEE1394 bus. Accordingly, the bus bridge 3 shown in FIG. 1 supports both the PCI-card bus bridge function and the PCI-1394
20 (OHCI-Link) bridge function.

The bus bridge 3 includes an arbiter 36 that arbitrates when the access demands of the card bus blocks 23A and 23B and the IEEE1394 block 35 compete with each other.

25 A bus slave 31 and a bus master 32 are

provided in the bus bridge 3. The data and address of the primary-side buses 21 are given from the bus slave 31 to the secondary-side bus. The data and address of the secondary-side buses 21 are given to the primary-
5 side bus through the bus master 32. The data given to the primary-side bus 21 through the bus master 32 is taken by a host 1 through the bus slave 11. When an access demand CREQQ# is given from the card buses 23A and 23B, each of the card bus blocks 33 and 34 takes the
10 access demand by the internal slave provided therein, and gives the access right to the arbiter 36. The arbiter 36 performs arbitration as described later, and outputs the acknowledgement ACK to the permitted card-bus block. The card-bus blocks 33 and 34 give enabling
15 signal CGNT # from the master thereof to the card bus 23A or 23B based on the acknowledgement ACK obtained from the arbiter 36.

The card bus 23A and 23B give the address and data to the respective bus blocks 33 and 34 based on an
20 enabling signal CGNT#. Then, the bus blocks 33 and 34 send the address and data to the primary side bus 21 through the bus master 32.

When the access demand REQ1394 is given from the 1394(PHY) bus 22, the IEEE1394 block 35 outputs the
25 access demand REQ1394 to the arbiter 36. When

arbitration is done as described later and an access right is given to the 1394(PHY) bus 22, the arbiter 36 outputs the acknowledgement ACK1394 to the IEEE1394 block 35. Then, the IEEE1394 block 35 gives the access
5 right to the 1394(PHY) bus 22 based on the signal ACK1394 supplied by the arbiter 36. Accordingly, the 1394(PHY) bus 22 can send the address and data to the primary side bus 21 based on the access demand REQ1394 through the IEEE1394 block 35 and the bus master 32.

10 As mentioned above, since the card buses 23A and 23B are connected (two card slots are provided in the bus bridge 3), there are three agents who can send the access demand REQ to the primary-side bus 21 from the secondary-side bus.

15 Generally, the arbitration refers to the mediation as to which master is given an access right when one bus is connected to a plurality of bus masters that compete with each other. However, in the present embodiment, the secondary-side buses are separate to
20 each other, and, thereby, the secondary-side buses do not compete with each other. The arbiter 36 in the present embodiment mediates which becomes a bus master with respect to the primary side buses 21 when more than two of the three secondary-side buses lodge the access
25 demand REQ at the same time.

If a priority is given to the IEEE1394 bus 22, which occupies the primary-side bus 21 for a long time, there may be a case in which the access right cannot be transferred to the card buses 23A and 23B or probability
5 of transferring the access right to the card buses 23A and 23B is extremely lowered, which prevents a normal data transfer.

When more than two access demands REQs are supplied at the same time, a priority is not given to
10 the IEEE1394 block 35, and the access right is given equally at the same rate with other blocks. Therefore, the arbiter 36 is provided with a counter so as to give the access right sequentially to the buses that have lodged the access demands REQs.

15 As a result, the acknowledgement ACK is supplied to the secondary-side buses equally at the same rate. Therefore, the access right is given sequentially to the secondary-side buses, and the arbiter 36 does not need a special circuit.

20 When the access right is given to the IEEE1394 bus 22 and when an operation is being performed through the IEEE1394 bus, the operation cannot be stopped due to the feature of the operation according to IEEE1394 until the operation according to the IEEE1394 is completed
25 even if the access demand REQ is supplied from the card

bus 23A or 23B.

Accordingly, when the IEEE1394 bus 22 is busy, the arbiter 36 is prevented from taking up the access right from the IEEE1394 bus 22. This can be achieved by
5 inputting a busy signal representing the busy state of the IEEE1394 bus 22 to the arbiter 36 so as to monitor the busy signal by the arbiter 36. However, in the present embodiment, the busy signal representing the busy state of the IEEE1394 bus 22 is reflected in the
10 access demand REQ. Specifically, the access demand IEEE1394 is lowered when the busy signal falls, and sequentially, the acknowledgement ACK1394 is lowered when the busy signal falls. Accordingly, the acknowledgement ACK1394 is continuously supplied until
15 the access demand REQ1394 is withdrawn. Thus, when the IEEE1394 bus is busy, the access right given to the IEEE1394 bus 22 is not taken up without increasing the number of inputs to the arbiter 36.

Since each of the card buses 23A and 23B is an
20 isolated bus besides the IEEE1394 bus 22, the acknowledgement ACK can be sent back to the card buses 23A and 23B. However, because the use right of the primary-side bus 21 is not given to the card buses 23A and 23B, the card buses 23A and 23B cannot transfer data.
25 Therefore, it is necessary to make the card buses 23A

and 23B to send the access demand again.

When the card bus is used as the secondary-side bus as is in the above-mentioned embodiment, signals CREQ# and CGNT# corresponding to the access demand REQ and the acknowledgement ACK, respectively, are input. Thus, the signals CREQ# and CGNT# may be input to the arbiter 36 instead of inputting the access demand REQ and the acknowledgement ACK. However, it should be noted that a car has to be taken since the polarities of the low and high levels are reversed.

When the number of agents who lodge the access demand REQ is greater than three as in the above-mentioned embodiment, the circuit structure of the bus bridge 3 becomes complex. In order to simplify the circuit structure, the arbiter 36 is divided into two stages of a first arbiter 36a and a second arbiter 36b as shown in FIG. 3. The first arbiter 36a performs the arbitration of the access demands of the IEEE1394 bus and the card buses 23A and 23B. The second arbiter 32b performs the arbitration of the access demands of the card buses 23A and 23B only at a time when Card-ACK is returned.

According to the above-mentioned structure of the arbiter 36, each of the arbiter 36a and 36b merely performs the selection of one from two. Therefore, the

first arbiter 36a and the second arbiter 36b can be composed of the same circuit. At this time, Card-REQ can be simply generated by an OR operation of REQ-A and REQ-B.

5 However, according to the above-mentioned structure, the access right cannot be given to the three agents equally. Specifically, in the above-mentioned embodiment, the ratio of slot A : slot B : IEEE1394 becomes equal to the ratio 1 : 1 : 2.

10 Although some systems allow such a distribution of the access right, when a completely equalized distribution of the access right is desired, the distribution ratio of the first arbiter 36a must be such that the ratio of card bus to the IEEE1394 bus
15 (card bus : IEEE1394) must be equal to 2 : 1. In order to achieve this scheme, a switch bit is merely provided to the internal register of the bus bridge. Moreover, there is no need to make a special arbitration circuit. For example, the arbitration circuit may give the access
20 right to the IEEE1394 bus after giving the access right to the card buses twice.

 In the above-mentioned bus bridge 3, malfunction may occur due to a collision on the primary-side bus when the access from the secondary-side bus is
25 accepted during the arbitration of the secondary-side

buses. In such a case, the access of the primary-side bus should not be retried (canceling the access without accepting the access at that time, and the same access is reissued later), and the current access of the

5 primary-side bus should be given a highest priority.

The reason for this is not to be able to give priority to the access of the secondary-side bus because it is uncertain what process is done by the bus master of the primary-side bus.

10 As for the operation of the arbiter 36, it is ideal not to return the acknowledge ACK to the secondary-side bus. However, there may be a case in which the arbiter has already sent the acknowledgement ACK to the card busses. In such a case, the card buses
15 are rendered to send the access demand again.

Alternatively, the access of the secondary-side bus can be accepted by setting separate paths to the process of the data path of the primary side and the data path of the secondary side.

20 However, since the primary-side bus is not in an idle state, the data to be transferred must be held in the bus master 2 of the bus bridge 3. In the case of the IEEE1394 bus, since the retry cannot be done on the secondary side, the access from the primary side is an
25 access to the internal register. Thus, there is no need

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WHAT IS CLAIMED IS:

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1. An arbitration method of a bus bridge
which interfaces a primary-side bus with a plurality of
secondary side buses, the primary-side bus being a local
bus in a system and the secondary-side buses being
10 external buses connected to the system, the bus bridge
supporting a plurality of kinds of operations one of
which is an operation related to a serial bus in
accordance with IEEE1394, the arbitration method
comprising the step of giving an access right equally to
15 each of the secondary-side buses, when access demands to
the primary-side bus are lodged from more than two of
the secondary-side buses at the same time, by not giving
a priority to any one of the secondary-side buses.

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2. The arbitration method as claimed in claim
1, wherein one of the secondary-side buses is the serial
bus in accordance with IEEE1394, and the rest of the
25 secondary-side buses are card buses.

3. The arbitration method as claimed in claim 1, further comprising the step of:

giving a priority right to the serial bus in accordance with IEEE1394; and

5 maintaining the access right given to the serial bus in accordance with IEEE1394 when an access demand is lodged from the secondary-side buses other than the serial bus in accordance with IEEE1394.

10

4. The arbitration method as claimed in claim 2, wherein the secondary-side buses include a plurality of card buses, and the arbitration method comprising the steps of:

performing an arbitration between the serial bus in accordance with IEEE1394 and the card buses when access demands are lodged from the serial bus in
20 accordance with IEEE1394 and the card buses; and

performing an arbitration between the card buses when an access right is to be given to one of the card busses.

25

5. The arbitration method as claimed in claim 1, further comprising the step of changing an order of giving the access right.

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6. The arbitration method as claimed in claim 1, further comprising the step of giving a highest
10 priority to the primary side bus when the primary-side bus lodges an access demand to the secondary-side buses irrespective of a condition of arbitration between the secondary side buses.

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7. An arbitration method of a bus bridge which interfaces a primary-side bus with a plurality of
20 secondary-side buses, the primary side bus being a local bus in a system and the secondary-side buses being external buses connected to the system, the bus bridge supporting a plurality of kinds of operations one of which is an operation related to a serial bus in
25 accordance with IEEE1394, the arbitration method

comprising the step of giving a highest priority to the primary-side bus when the primary side bus lodges an access demand to the secondary-side buses irrespective of a condition of an arbitration between the secondary-side buses.

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A bus bridge interfaces a primary-side bus with a plurality of secondary-side buses. The primary side bus is a local bus in a system and the secondary-side buses are external buses connected to the system. The bus bridge supports a plurality of kinds of operations one of which is an operation related to a serial bus in accordance with IEEE1394. An access right is given equally to each of the secondary-side buses, when access demands to the primary-side bus are lodged from more than two of the secondary side buses at the same time, by not giving a priority to any one of the secondary side buses.

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FIG. 1

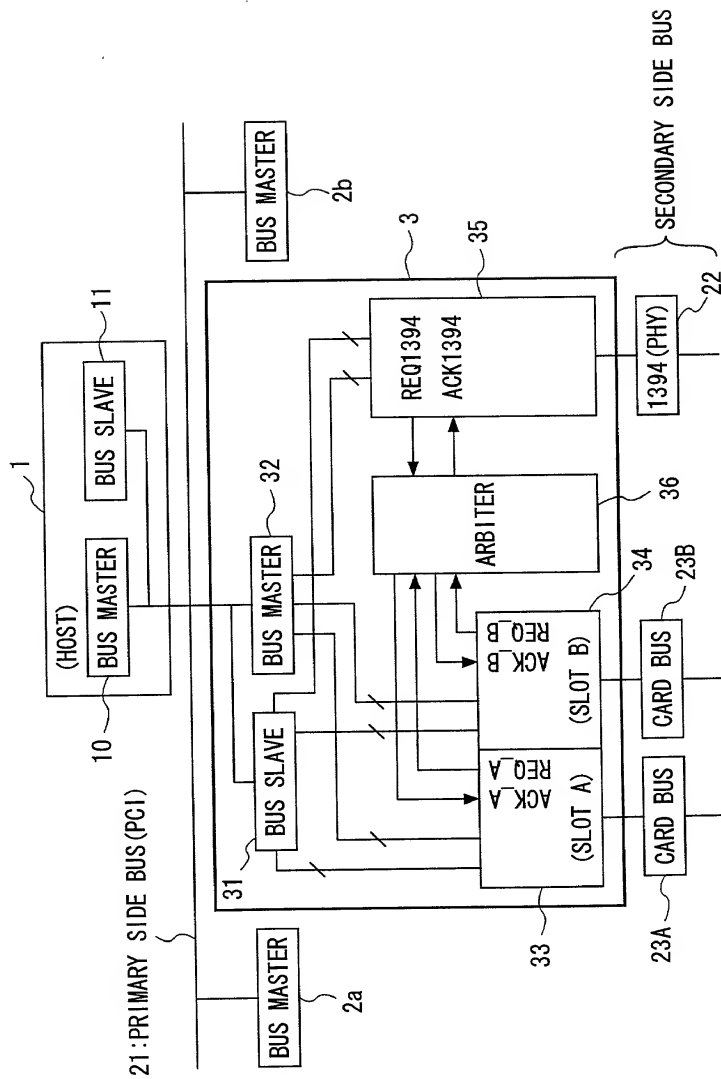


FIG.2

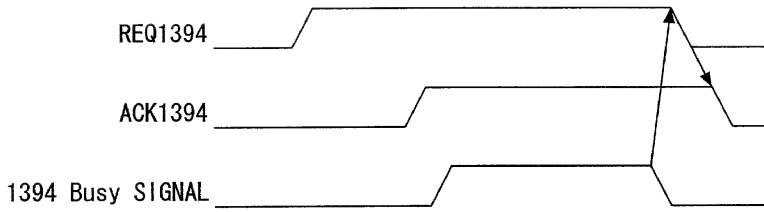
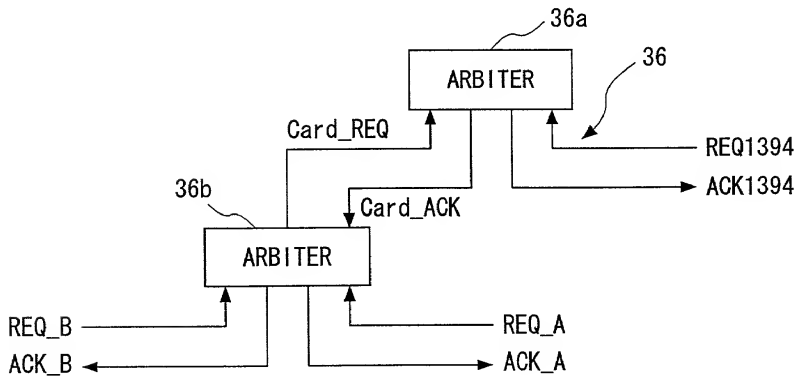


FIG.3



Docket No.

Declaration and Power of Attorney For Patent Application

English Language Declaration

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

ARBITRATION METHOD OF A BUS BRIDGE

the specification of which

(check one)

☒ is attached hereto.

☐ was filed on _____ as United States Application No. or PCT International

Application Number _____

and was amended on _____

(if applicable)

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, Code of Federal Regulations, Section 1.56.

I hereby claim foreign priority benefits under Title 35, United States Code, Section 119(a)-(d) or Section 365(b) of any foreign application(s) for patent or inventor's certificate, or Section 365(a) of any PCT International application which designated at least one country other than the United States, listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate or PCT International application having a filing date before that of the application on which priority is claimed.

Prior Foreign Application(s)

Priority Not Claimed

Patent Application
No. 11-190944

Japan

5/July/1999

☐

(Number)

(Country)

(Day/Month/Year Filed)

☐

(Number)

(Country)

(Day/Month/Year Filed)

☐

(Number)

(Country)

(Day/Month/Year Filed)

I hereby claim the benefit under 35 U.S.C. Section 119(e) of any United States provisional application(s) listed below:

(Application Serial No.)

(Filing Date)

(Application Serial No.)

(Filing Date)

(Application Serial No.)

(Filing Date)

I hereby claim the benefit under 35 U. S. C. Section 120 of any United States application(s), or Section 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of 35 U.S.C. Section 112, I acknowledge the duty to disclose to the United States Patent and Trademark Office all information known to me to be material to patentability as defined in Title 37, C. F. R., Section 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of this application:

(Application Serial No.)

(Filing Date)

(Status)
(patented, pending, abandoned)

(Application Serial No.)

(Filing Date)

(Status)
(patented, pending, abandoned)

(Application Serial No.)

(Filing Date)

(Status)
(patented, pending, abandoned)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

Gary M. Hoffman, 26,411; Thomas J. D'Amico, 28,371; Donald A. Gregory, 28,954; James W. Brady, Jr., 32,115; Jon D. Grossman, 32,699; Mark J. Thronson, 33,082; Laurence D. Fisher, 37,131; John R. Fuisz, 37,327; Juliana Haydoutova, P43,313; James M. Heintz, P41,828; Herbert V. Kerner, P42,721; Gianni Minutoli, 41,198; Eric Oliver, 35,307; William E. Powell, III, 39,803; James M. Silbermann, 40,413; Richard Veltman, 36,957 and Darius Gambino, 41,472.

Send Correspondence to:

Dickstein Shapiro Morin & Oshinsky LLP
2101 L Street, N.W.
Washington, D.C. 20037-1526

Direct Telephone Calls to:

202-785-9700

Full name of sole or first inventor	
KOJI KAMEDA	
Sole or first inventor's signature	Date
<i>Koji Kameda</i>	June 21, 2000
Residence	
Hyogo, Japan	
Citizenship	
Japan	
Post Office Address	
c/o RICOH COMPANY, LTD.,	
3-6, Nakamagome 1-chome, Ohta-ku, Tokyo 143-8555, Japan	

Full name of second inventor, if any	
Second inventor's signature	Date
Residence	
Citizenship	
Post Office Address	